MIX2007 3W Mono Class-D Audio Power Amplifier

Description

The MIX2007 is a high efficiency, filter-less 3W Mono class-D audio power amplifier. The high power output stage suitable for portable devices.

The filter-less architecture and the few external components for operation save the board space and cost.

The MIX2007 features shutdown and mute control functions. The Over temperature protection function and Low POP noise make system design more flexibility.

The MIX2007 is available in SOP8 package .

Features

- Output Power: -3W (VDD=5.0V, RL =4 Ω, THD+N=10%)
- Supply Voltage : 2.5V to 5.5V
- Low Noise and Low distortion
- Low Pop noise
- Shutdown current less than 1uA
- Over temperature protection

Application

- Bluetooth devices
- Mini-Speaker USB Speaker
- 2.0 / 2.1 Speaker

Typical Application VDD 1uF VDD Audio Ci Ri Input IN-Ŵ OUT+ MIX2007 **OFF** SD OUT-ON Bypass GND 0.1u

Pin Configuration



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Pin Descriptions

Pin	Symbol	I/O	Descriptions
1	SD	I	Shutdown Control Pin (low for active)
2	NC		No connection
3	BYPASS	I/O	Analog reference bypass capacitor
4	-IN	I	Negative Audio Input
5	OUTN	0	Audio Negative Output
6	VDD		VDD
7	GND		GND
8	OUTP	0	Audio Positive Output

Ordering Information

Order Number	Package	Marking	Packing
MIX2007	8000	MIX2007	2500pcs/reel
WIX2007	3000	XXXXXXX	100pcs/tube

Absolute Maximum Ratings

V _{DD}	Supply voltage	-0.3V to 6V
VI	Input voltage	-0.3V to V _{DD} +0.3V
T _A	Operation free-air temperate range	-40°C to 85°C
TJ	Operation free-air junction temperature	-40°C to 125°C
T _{STG}	Storage temperature range	-65°C to 150°C
T _{SLD}	Soldering temperature	300°C, 5sec

Recommended Operating Conditions

			MIN	MAX	UNIT
V_{DD}	Supply Voltage	VDD	2.5	5.5	V
V _{IH}	SD Input High Level	$V_{DD}=5.0V$	2		V
VIL	SD Input Low Level	$V_{DD}=5.0V$		0.6	V

Thermal Information

Parameter	Symbol	Package	MAX	UNIT
Thermal Resistance(Junction to Ambient)	θ_{JA}	SOP8	115	°C/W

Electrical Characteristics

Symbol	Parameter	Test Conditions		MIN	TYP	MAX	UNIT
V _{IN}	Supply Power			2.5	-	5.5	V
		THD+N=10%,f=1KHZ,R _L =4Ω	$V_{DD}=5.0V$		3		w
			V _{DD} =3.6V		1.55		
			V _{DD} =5.0V		2.5		w
Р	Output Dower	$I H D + N = 1\%, I = I K H Z, K = 4\Omega$	V _{DD} =3.6V		1.28		
P0		THD+N=10%,f=1KHZ,R _L =8Ω	V _{DD} =5.0V		1.72		w
			V _{DD} =3.6V		0.9		
			V _{DD} =5.0V		1.4		w
		$IHD + N = 1\%, I = 1KHZ, R_{L} = 8\Omega$	V _{DD} =3.6V		0.75		
		V_{DD} =5.0V, P_O =1W, R_L =4 Ω	£ 41411-		0.02		- %
TUDIN	Total Harmonic Distortion	V_{DD} =3.6V, P _O =0.5W, R _L =4 Ω	t=1KHZ		0.02		
THD+N	Plus Noise	V_{DD} =5.0V, P ₀ =0.5W, R _L =8 Ω			0.013		
		V_{DD} =3.6V, P _O =0.25W, R _L =8 Ω	T=1KHZ		0.013		
Gv	Gain		Ri=30K		20		dB
PSRR	Power-supply ripple rejection	VDD=4.2V ±200mVp-p	f=1KHz		65		dB
SNR	Signal-to-Noise Ratio	V _{DD} =5.0V,Vo rms=1V, G _V =20dB	f=1KHz		-85		dB
		V _{DD} =5.0V,Input floating with	A-weighting		60		
vn	Output Noise	C _{IN} =0.1µF	NoA-weighting		80		μν
Dyn	Dynamic range	V _{DD} =5.0V,THD=1%	f=1KHz		-92		dB
		R _L =8Ω, Po=1.5W			90		- %
η	Efficiency	R _L =4Ω, Po=2.5W	t=1KHZ		85		
	Quiescent Current	V _{DD} =5.0V			4		- mA
IQ		V _{DD} =3.0V	NO LOAD		2.7		
I _{SD}	Shutdown Current	V _{DD} =2.5V to 4.2V	V _{SD} =3.3V			1	μA
Vos	Offset Voltage	V _{IN} =0V, V _{DD} =5V			10		mV
Fosc	Oscillator frequency				650		khz
Tst	Startup time	Bypass capacitor =10nF			15		mS
OTP	_	No Load, Junction			180		•••
ОТН	—	Temperature	V _{DD} =5.0V		40		-0

(VDD =5V, Gain=20dB, R_L =8 Ω , T =25°C, unless otherwise noted.)

Typical Operating Characteristics

(VDD =5V, Gain=20dB, R_L =8 Ω , T =25°C, unless otherwise noted.)





THD+N VS FREQUENCY



NOISE FLOOR FFT 4ρ -40 RL=80hm Gain=20dB -60 d B V -80 -100 -120 50 100 200 500 5k 10k 20k 10 20 2k 1k Hz

Frequency Response



Application Information

Input Resistors (R_I)

The input resistors (R_I) set the gain of amplifier according to the below equation:

Gain = 2 X
$$\frac{\text{Rf}}{\text{RI}}$$
 $\left(\frac{\text{V}}{\text{V}}\right)$

Rf is 150K Ω and the Ri is the external input resistor. For example, if you put a 30K Ω resistor at the input stage. The gain is below:

Gain =
$$\frac{2 \times 150 \text{K}\Omega}{30 \text{K}\Omega} = 10 \left(\frac{\text{V}}{\text{V}}\right) = 20 \text{dB}$$

Please place the input resistor very close to the MIX2007 for better noise performance.

Input Capacitors (C_i)

The input capacitor and input resistor from a high-pass filter with the corner frequency, f_c , determined in below equation:

$$f_{c} = \frac{1}{(2\pi RiCi)}$$

The value of input capacitor is important to consider as it directly affects the bass (low frequency) performance of the circuit.

Below equation is used to solve the input coupling capacitance.

$$C_{I} = \frac{1}{(2\pi Rifc)}$$

The capacitors should have tolerance of $\pm 10\%$ or better, because any mismatch in capacitance causes an impedance mismatch at the corner frequency.

Analog Reference Bypass Capacitor (C_{BYP})

The Analog Reference Bypass Capacitor (C_{BYP}) is the most critical capacitor and serves several important functions. During start-up or recovery from shutdown mode, C determines the rate at which the amplifier starts up. The second function is to reduce noise caused by the power supply coupling into the output drive signal. This noise is from the internal analog reference to the amplifier, which appears as degraded PSRR and THD+N.

A ceramic bypass capacitor (C_{BYP}) with values of 0.47 μ F to 1.0 μ F is recommended for the best noise performance. Increasing the bypass capacitor reduces clicking and popping noise from power on/off and entering and leaving shutdown.

Shutdown operation

The MIX2007 employs a shutdown function which consumption less than 1uA current to save the standby time. The SHDN input terminal should be pull low during normal operation when the amplifier is in use.

Thermal Protection

Thermal protection on the MIX2007 prevents damage to the device when the internal die temperature exceeds 180° C. There is a $\pm 30^{\circ}$ C tolerance on this trip point from device to device. Once the die temperature exceeds the thermal set point, the device enters into the shutdown state and the outputs are disabled. This is not a latched fault. The thermal fault is cleared once the temperature of the die is reduced by 30° C. The device begins normal operation at this point with no external system intervention.

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封装图(SOP8)



Symbol	Millime	ter(mm)	Inch		
Symbol	min	max	min	max	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	1.550	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.27(BSC)		0.050	(BSC)	
L	0.400	1.270	0.016	0.050	

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