



P-Channel Enhancement Mode Power MOSFET

Description

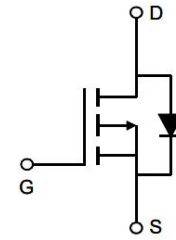
The PE7280G uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. It can be used in a wide variety of applications.

General Features

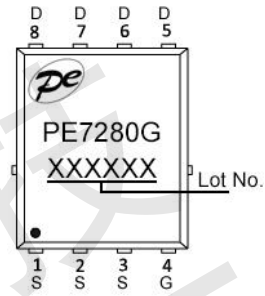
- $V_{DS} = -20V$, $I_D = -80A$
- $R_{DS(ON)} < 6m\Omega @ V_{GS}=-10V$
- $R_{DS(ON)} < 7.2m\Omega @ V_{GS}=-4.5V$
- $R_{DS(ON)} < 11.2m\Omega @ V_{GS}=-2.5V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

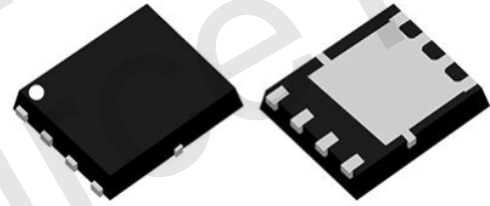
- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



DFN5x6-8L

Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	V
Drain Current-Continuous (TC=25°C)	I_D	-80	A
Drain Current-Continuous (TC=100°C)	I_D	-63	A
Pulsed Drain Current (Note 1)	I_{DM}	-240	A
Maximum Power Dissipation	P_D	83	W
Avalanche Current	I_{AS}	60	A
Avalanche Energy (L=0.1mH)	E_{AS}	180	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.5	°C/W
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Electrical Characteristics (TC=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-20	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-18V, V_{GS}=0V$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.45	-0.7	-1	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-20A$	-	5	6	m Ω
		$V_{GS}=-4.5V, I_D=-20A$	-	6	7.2	m Ω
		$V_{GS}=-2.5V, I_D=-15A$	-	8	11.2	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=-5V, I_D=-20A$	-	95	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$V_{DS}=-10V, V_{GS}=0V,$ $F=1.0MHz$	-	4210	-	pF
Output Capacitance	C_{oss}		-	650	-	pF
Reverse Transfer Capacitance (Note 4)	C_{rss}		-	380	-	pF
Switching Characteristics						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-10V, R_L=1\Omega,$ $V_{GS}=-10V, R_G=3\Omega$	-	18	-	nS
Turn-on Rise Time	t_r		-	32	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	126	-	nS
Turn-Off Fall Time	t_f		-	59	-	nS
Total Gate Charge	Q_g	$V_{DS}=-10V, I_D=-14A,$ $V_{GS}=-4.5V$	-	44	-	nC
Gate-Source Charge	Q_{gs}		-	9	-	nC
Gate-Drain Charge	Q_{gd}		-	11	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{GS}=0V, I_S=-1A$	-	-	-1.2	V
Diode Forward Current (Note 2)	I_S		-	-	-15	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to product.



Typical Electrical and Thermal Characteristics

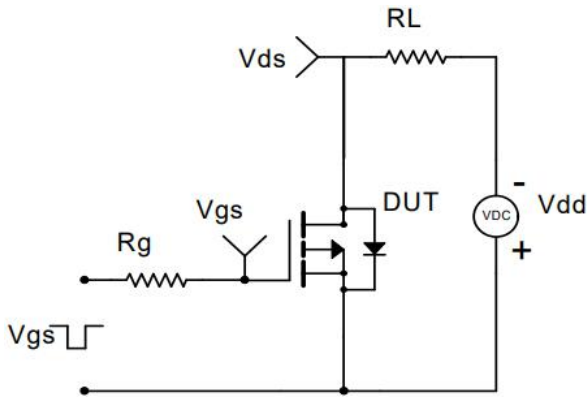


Figure 1 Switching Test Circuit

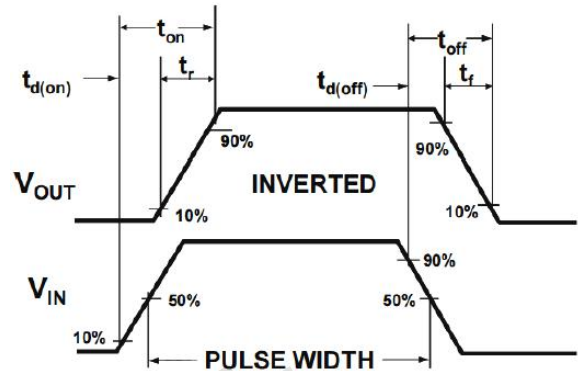


Figure 2 Switching Waveform

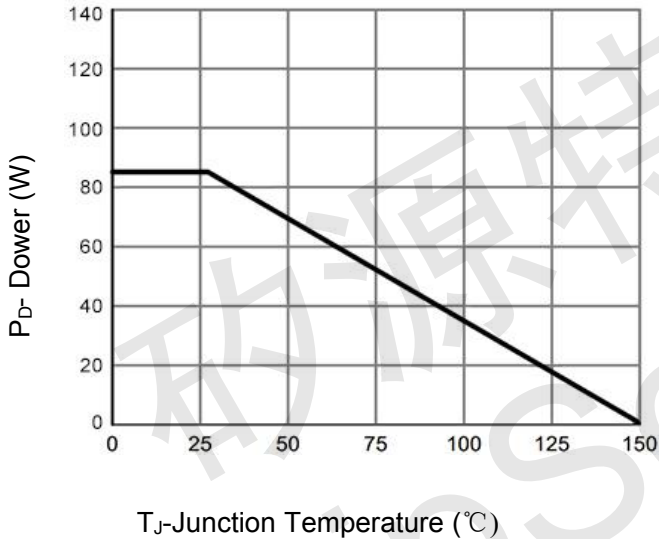


Figure 3 Power De-rating

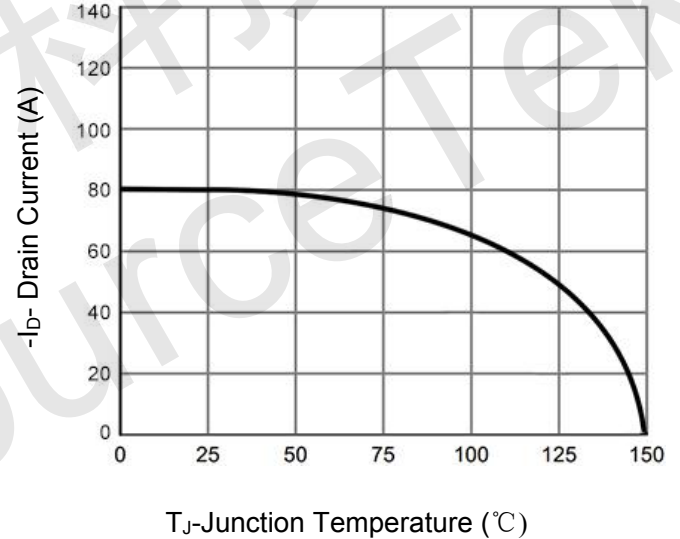


Figure 4 Drain Current

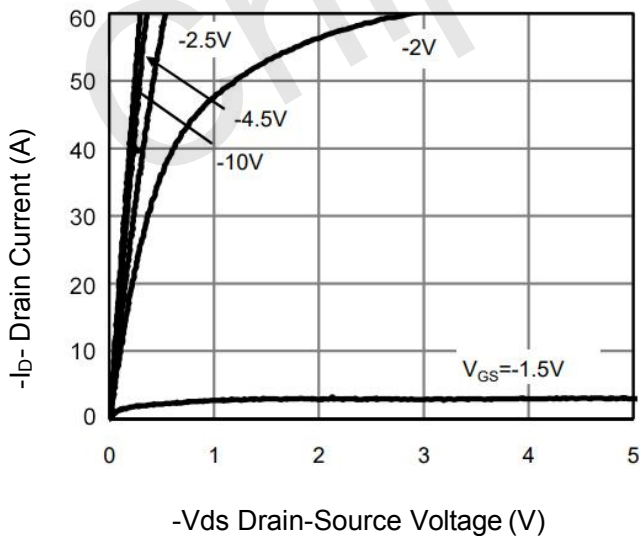


Figure 5 Output Characteristics

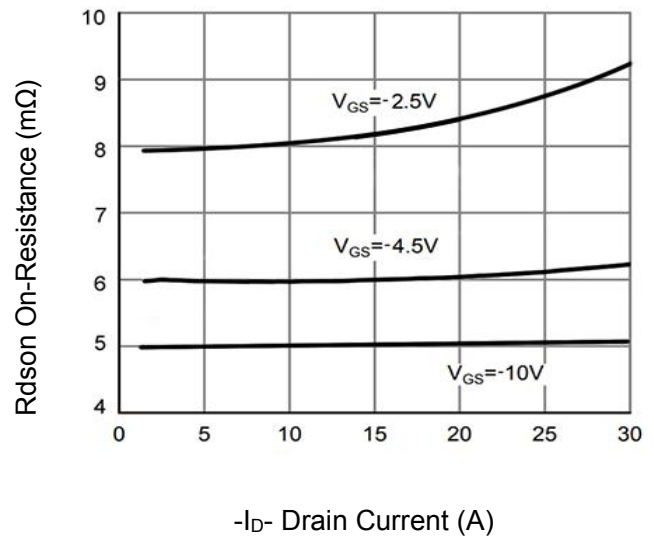


Figure 6 R_dson vs Drain Current

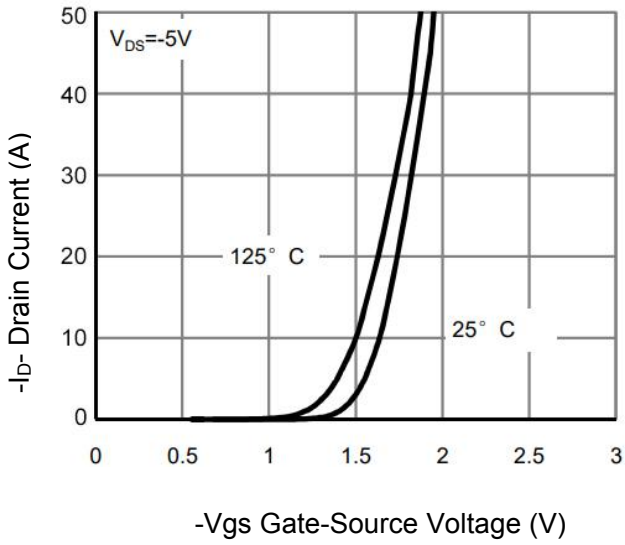


Figure 7 Transfer Characteristics

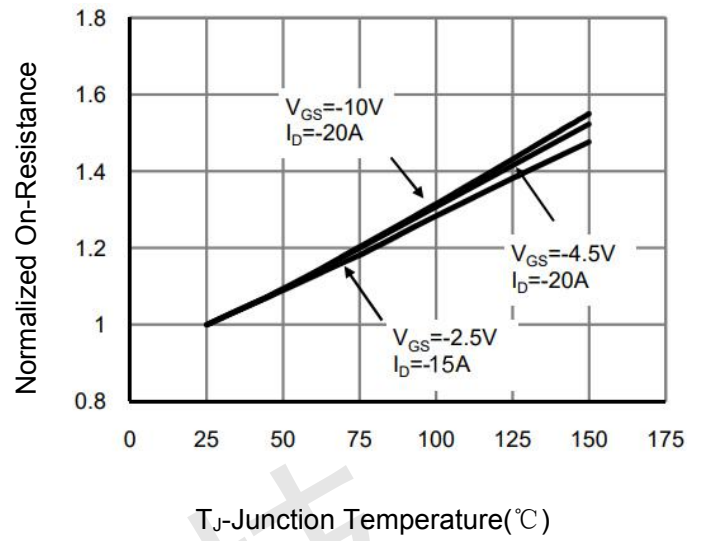


Figure 8 Rdson vs Junction Temperature

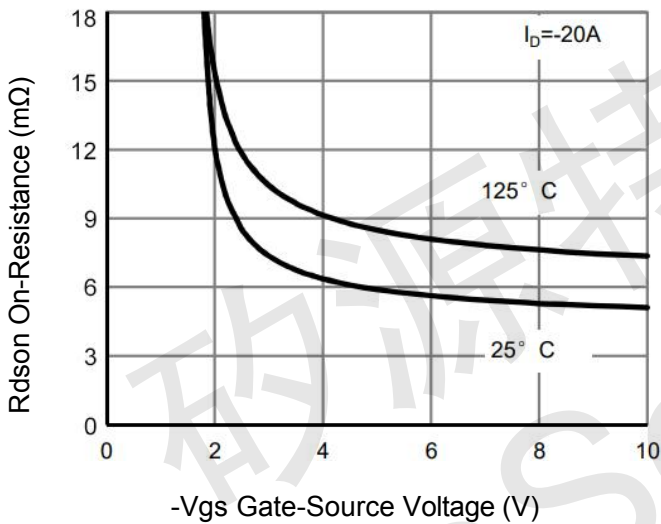


Figure 9 Rdson vs Vgs

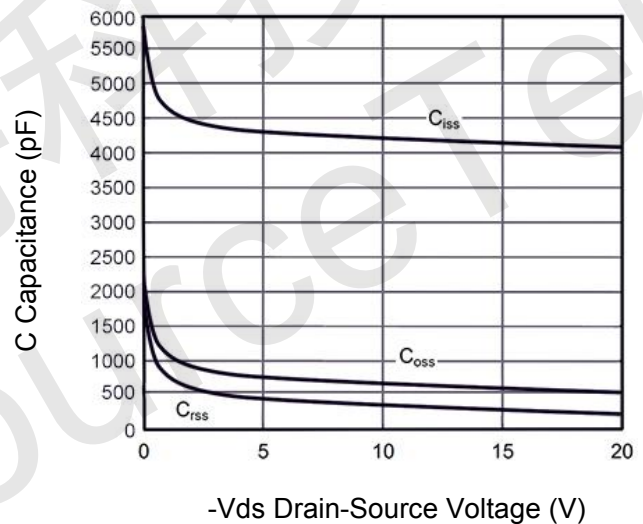


Figure 10 Capacitance vs Vds

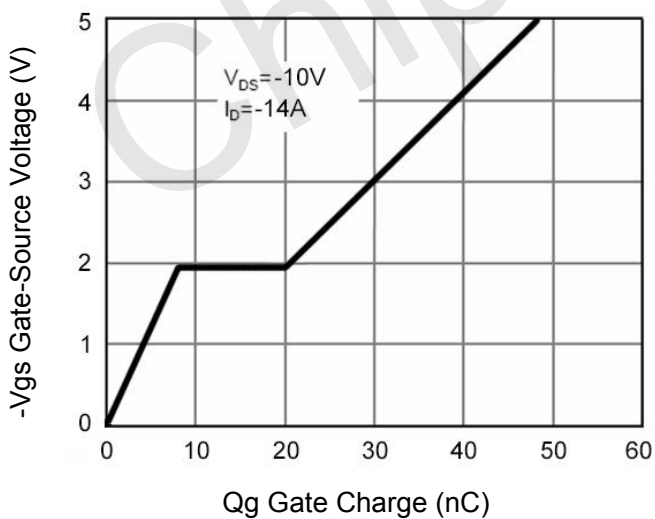


Figure 11 Gate Charge

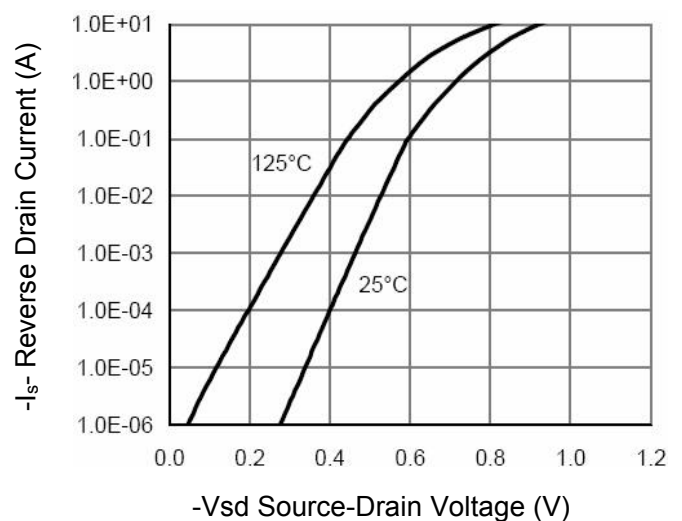


Figure 12 Source- Drain Diode Forward

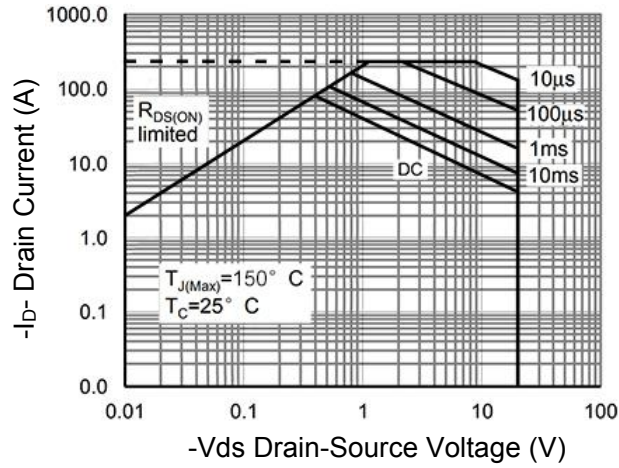


Figure 13 Safe Operation Area

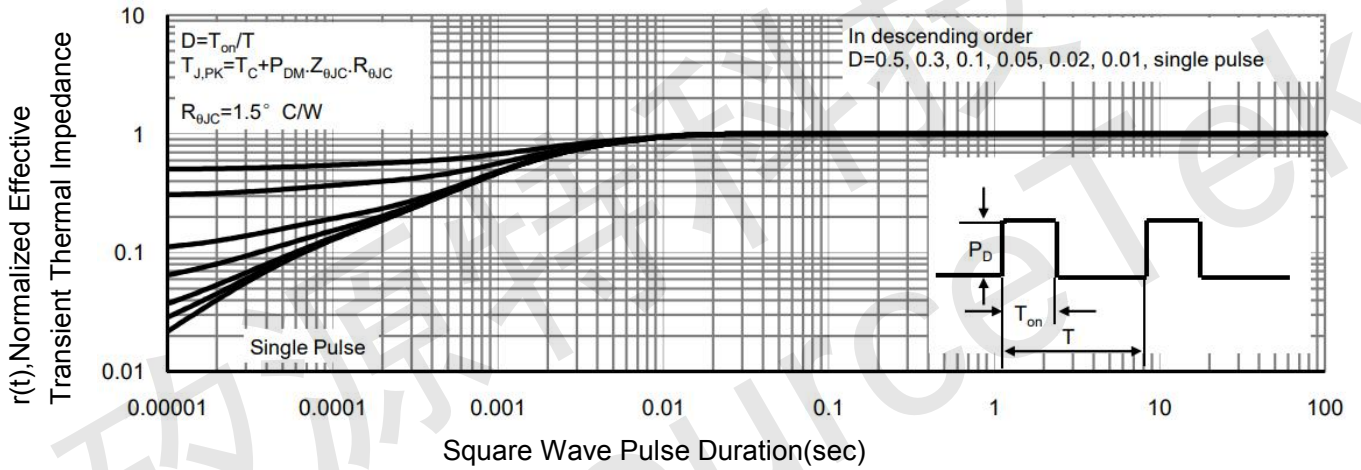
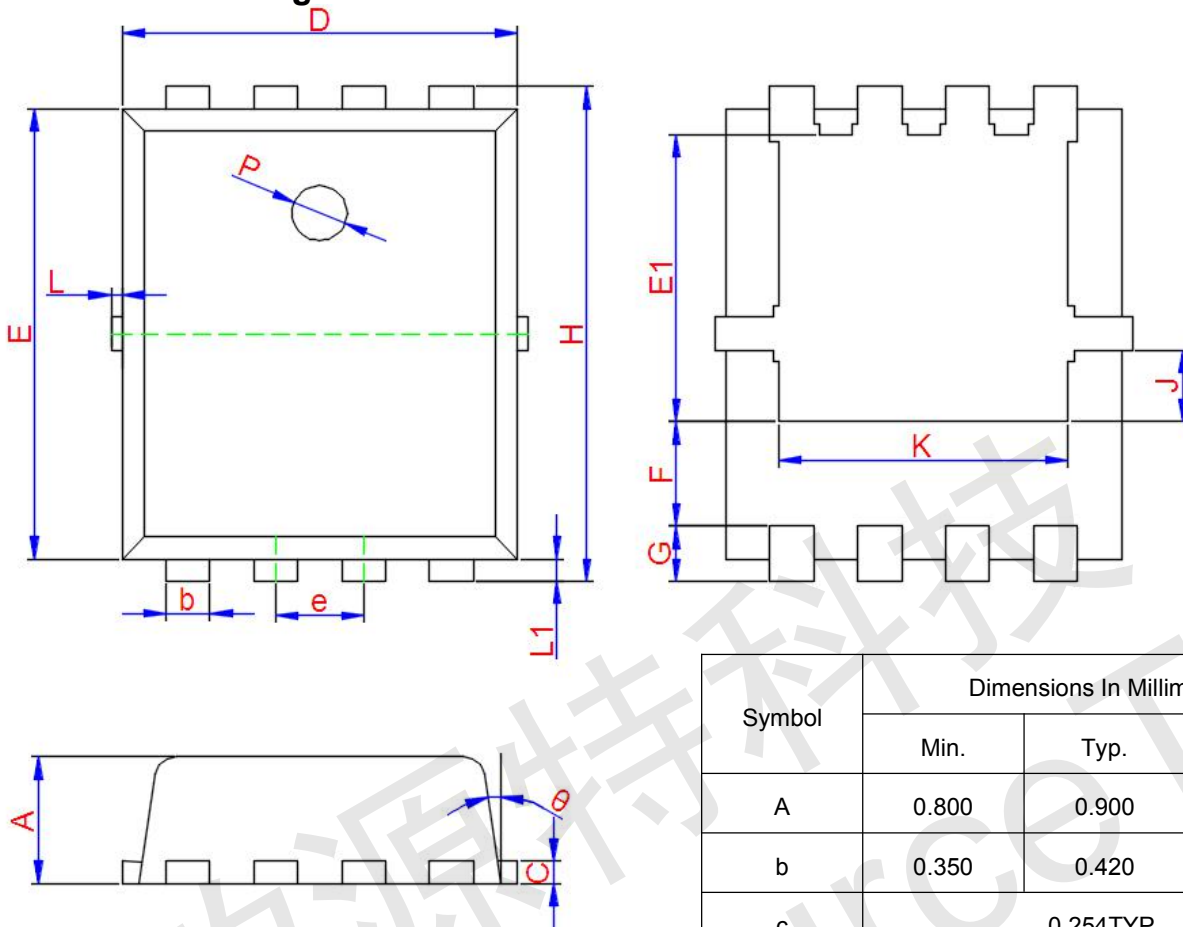


Figure 14 Normalized Maximum Transient Thermal Impedance



DFN5x6-8L Package Information



Symbol	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	0.800	0.900	1.000
b	0.350	0.420	0.490
c	0.254TYP.		
D	4.900	5.000	5.100
e	1.270TYP.		
E	5.700	5.800	5.900
E1	3.400TYP.		
F	1.400TYP.		
G	0.600TYP.		
H	5.950	6.080	6.200
J	0.950TYP.		
K	4.000TYP.		
L	-	-	0.150
L1	0.100	0.140	0.180
P	1.000TYP.		
θ	6°	10°	14°