



CST2N7002K N-Ch 60V Fast Switching MOSFETs

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent Cdv/dt effect decline
- ★ Advanced high cell density Trench technology



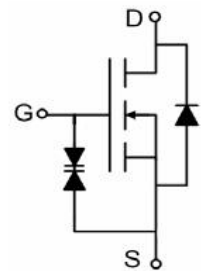
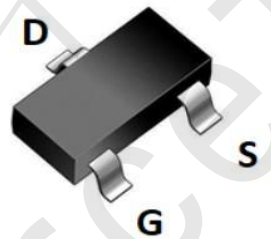
CST2N7002K Product Summary

BVDSS	RDSON	ID
60V	1.9 Ω	300m A

CST2N7002K Description

The CST2N7002K is the high cell density trenched N-ch MOSFETs, which provides excellent RDSON and efficiency for most of the small power switching and load switch applications. The CST2N7002K meet the RoHS and Green Product requirement with full function reliability approved.

CST2N7002K SOT23 Pin Configuration



CST2N7002K Absolute Maximum Ratings (TA=25°C unless otherwise specified)

Symbol	Parameter	Max.	Units
V _{DSS}	Drain-Source Voltage	60	V
V _{GSS}	Gate-Source Voltage	±20	V
I _D	Continuous Drain Current	T _A = 25°C	A
		T _A = 100°C	
I _{DM}	Pulsed Drain Current ^{note1}	1.2	A
P _D	Power Dissipation	T _A = 25°C	W
R _{θJA}	Thermal Resistance, Junction to Ambient	357	°C/W
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C



CST2N7002K N-Ch 60V Fast Switching MOSFETs

CST2N7002K Electrical Characteristics (T_J=25 °C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D = 250μA	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =60V, V _{GS} = 0V,	-	-	1	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±10	uA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	1	1.5	2.5	V
R _{DS(on)}	Static Drain-Source on-Resistance note2	V _{GS} =10V, I _D =0.3A	-	1.9	2.2	Ω
		V _{GS} =4.5V, I _D =0.2A	-	2.05	2.87	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	-	28	-	pF
C _{oss}	Output Capacitance		-	11	-	pF
C _{rss}	Reverse Transfer Capacitance		-	4	-	pF
Q _g	Total Gate Charge	V _{DS} = 10V, I _D = 0.3A, V _{GS} = 4.5V	-	1.7	-	nC
Q _{gs}	Gate-Source Charge		-	0.3	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	0.6	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} = 10V, I _D =0.2A, R _{GEN} = 10Ω,V _{GS} =10V,	-	2	-	ns
t _r	Turn-on Rise Time		-	15	-	ns
t _{d(off)}	Turn-off Delay Time		-	7	-	ns
t _f	Turn-off Fall Time		-	20	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	0.3	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	1.2	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S =0.3A	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%



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CST2N7002K Typical Performance Characteristics

Figure1: Output Characteristics

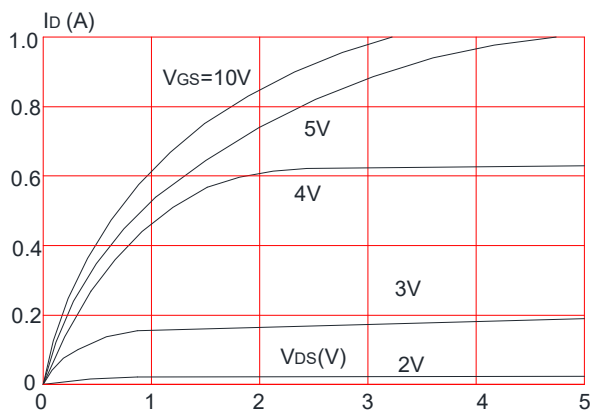


Figure 2: Typical Transfer Characteristics

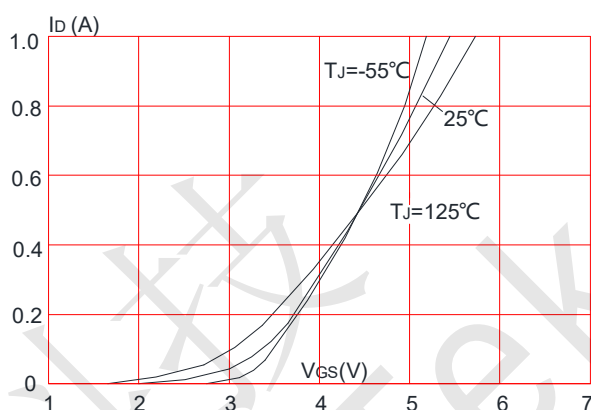


Figure 3: On-resistance vs. Drain Current

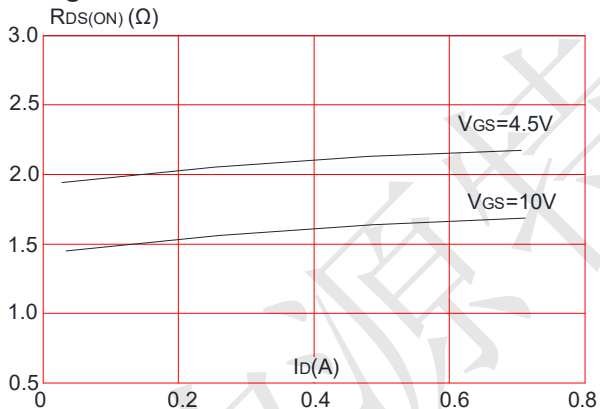


Figure 4: Body Diode Characteristics

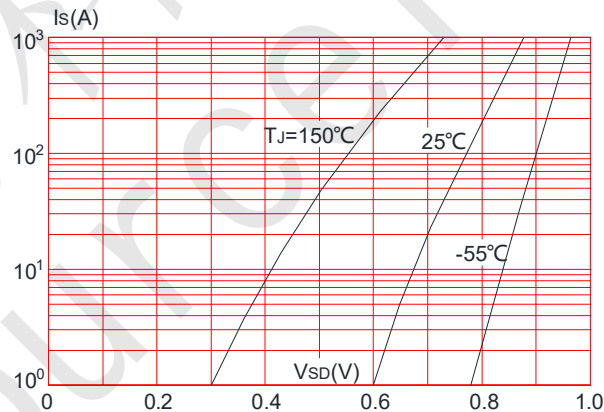


Figure 5: Gate Charge Characteristics

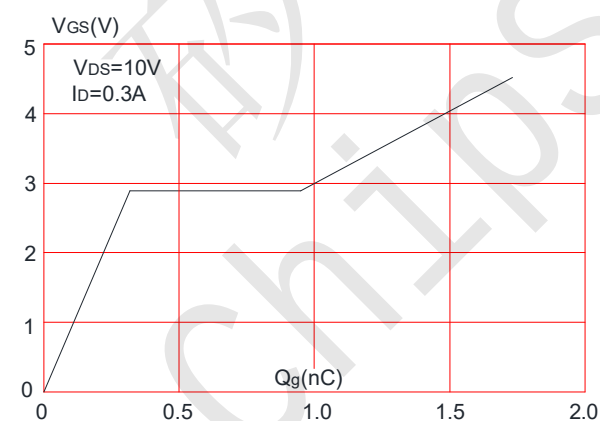
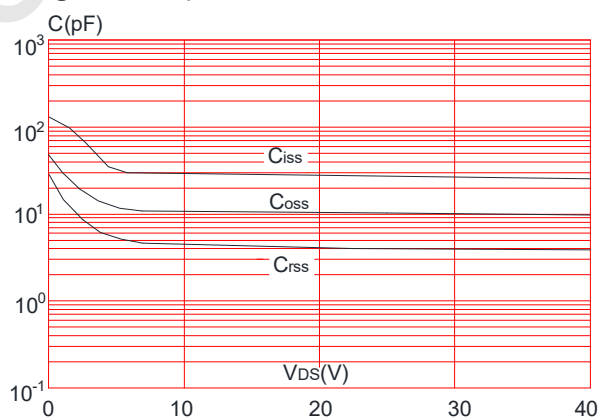


Figure 6: Capacitance Characteristics





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Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

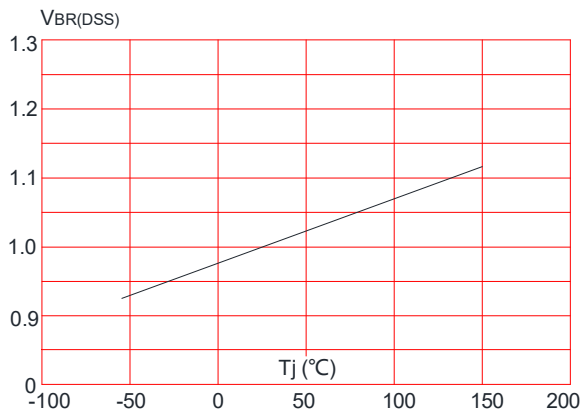


Figure 8: Normalized on Resistance vs. Junction Temperature

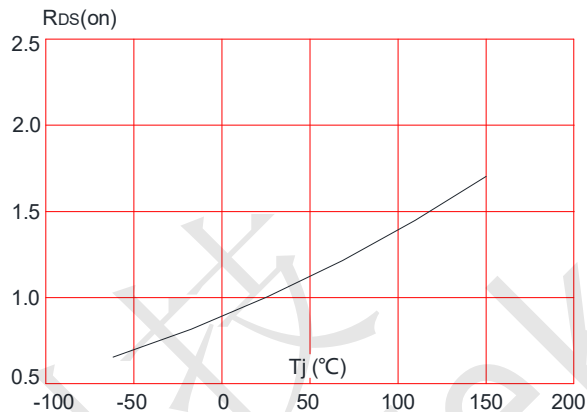


Figure 9: Maximum Safe Operating Area

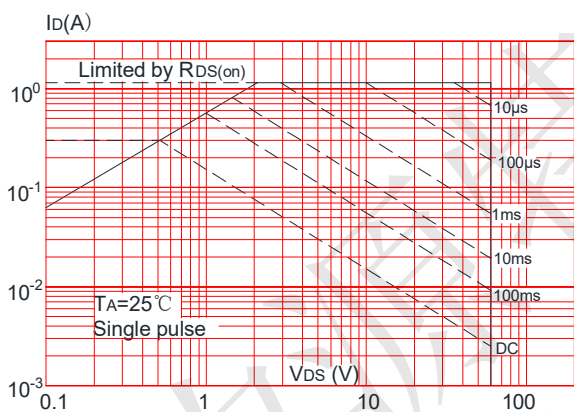


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

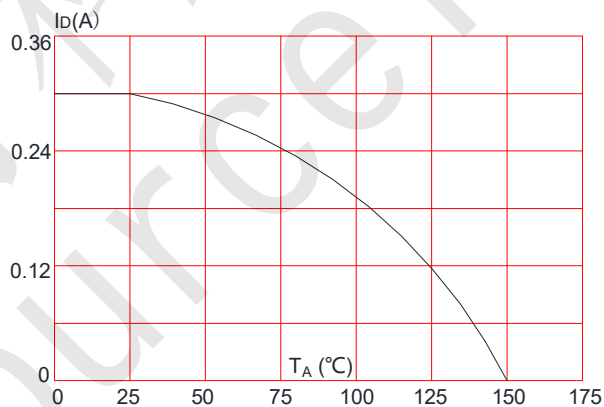
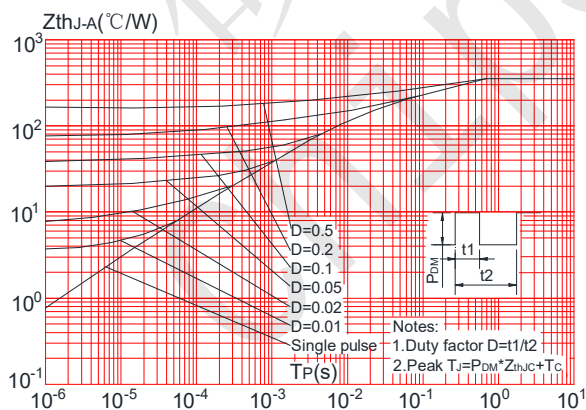


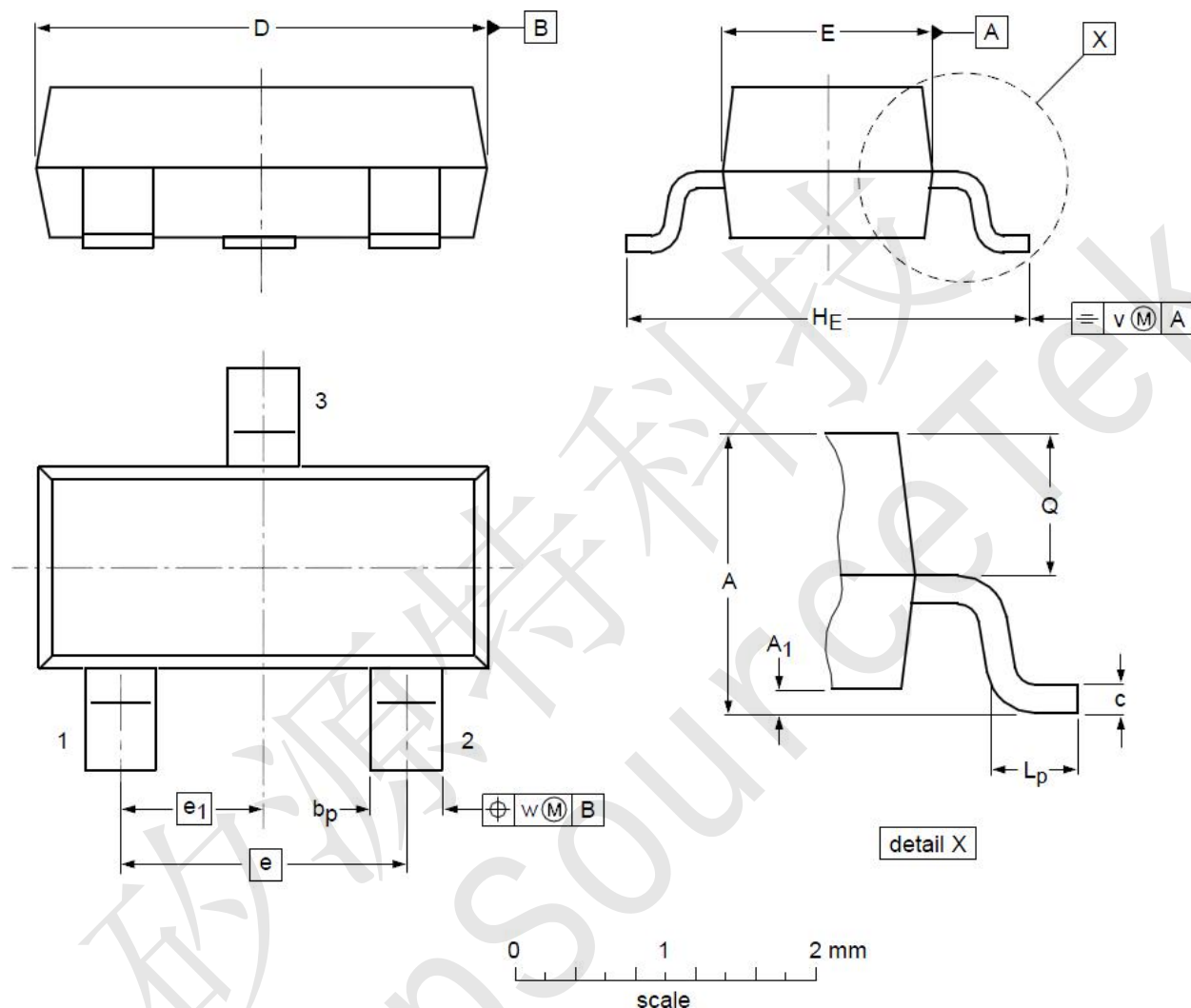
Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient





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CST2N7002K SOT23 Mechanical Data



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.01	1.15	A ₁	0.01	0.05	0.10
b _p	0.30	0.42	0.50	c	0.08	0.13	0.15
D	2.80	2.92	3.00	E	1.20	1.33	1.40
e	--	1.90	--	e ₁	--	0.95	--
H _E	2.25	2.40	2.55	L _p	0.30	0.42	0.50
Q	0.45	0.49	0.55	v	--	0.20	--
w	--	0.10	--				